

**In the Specification**

At page 1, before the Technical Field insert:

**CROSS REFERENCE TO RELATED APPLICATION**

This patent application is a Continuation Application of U.S. Patent Application Serial No. 10/232,794 filed August 28, 2002, entitled "Memory Integrated Circuitry," naming Luan C. Tran, and Alan R. Reinberg as inventors, which is a Divisional of U.S. Patent Application Serial No. 09/930,787, filed August 14, 2001, which is a Divisional Application of U.S. Patent Application Serial No. 08/842,230, filed April 22, 1997, now U.S. Patent No. 6,297,129, the disclosures of which are hereby incorporated herein by reference.

The paragraph beginning at p. 9, line 22 and extending through line 23 on page 9 has been amended as follows:

U.S. Patent Application Serial Number 08/530,661 listing Brent Keeth and Pierre Fazan as inventors discloses methods which can be utilized for forming the structures disclosed herein (such as, for example, dry oxidation for formation of LOCOS), and is hereby incorporated by reference.

At p. 11, line 2, after CLAIMS, insert: We claim: